



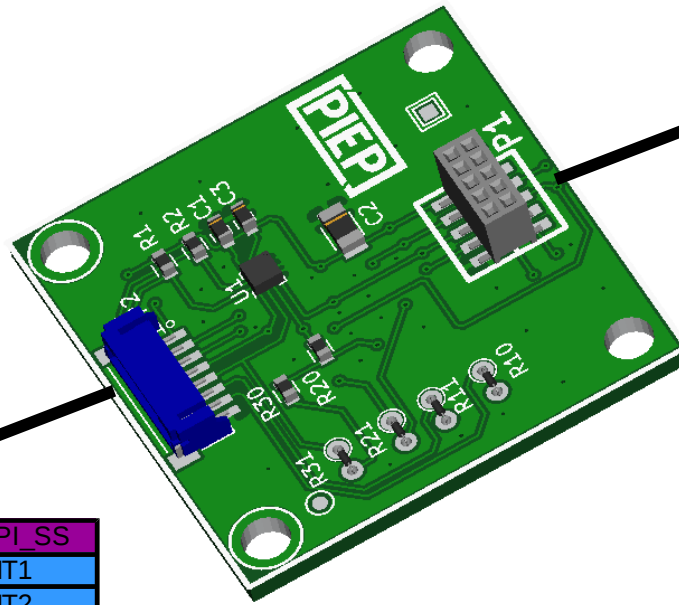
# PN 311-25, Rev B Accelerometer Board

## Stacking Header Pin Assignments

Top / Bottom Headers			
1	+3.3V		2
3		DIO (P1-0)*	4
5	I2C SDA / SDA (3)	I2C_SCL / SCL(2)	6
7			8
9	GND	IRQ (P1-2)**	10

I2C Address – 0x0c (SD0 low) or 0x0d (SD0 high)  
 \* Cut R21 to set alternate address of 0x0d; Removing R21 also frees SPIP3 header pin for other peripherals.

\*\*Device interrupts routed to SPIP-10. INT2 can be isolated from SPIP pin by cutting R10; INT1 can be isolated from SPIP pin by cutting R11



### Supplemental (SPI) Header (P2)

1	+3.3V	5	SPI_SS
2	SPI_MOSI	6	INT1
3	SPI_MISO	7	INT2
4	SPI_CLK	8	GND

Alternate connection method; do not connect if installed via stacking connector to a processor.

P2 permits remote installation and communications via SPI mode. Wire to processor via breakout board (PN 311-07). Mount breakout on processor stack and wire between breakout P1 connector and accelerometer P2.

To use in SPI mode, remove jumper R31  
 Isolate interrupts INT1&INT2 by removing either R10 or R11.

ST Micro type LIS2DH12 accelerometer board.

P2 mating connector – SKU 400-002  
 Pre-crimped leads – SKU 400-010  
 Additional Standoffs – SKU 400-012

PIEP is a copyright of E3 Embedded Systems, LLC and is protected under US Patent 9,870,337

