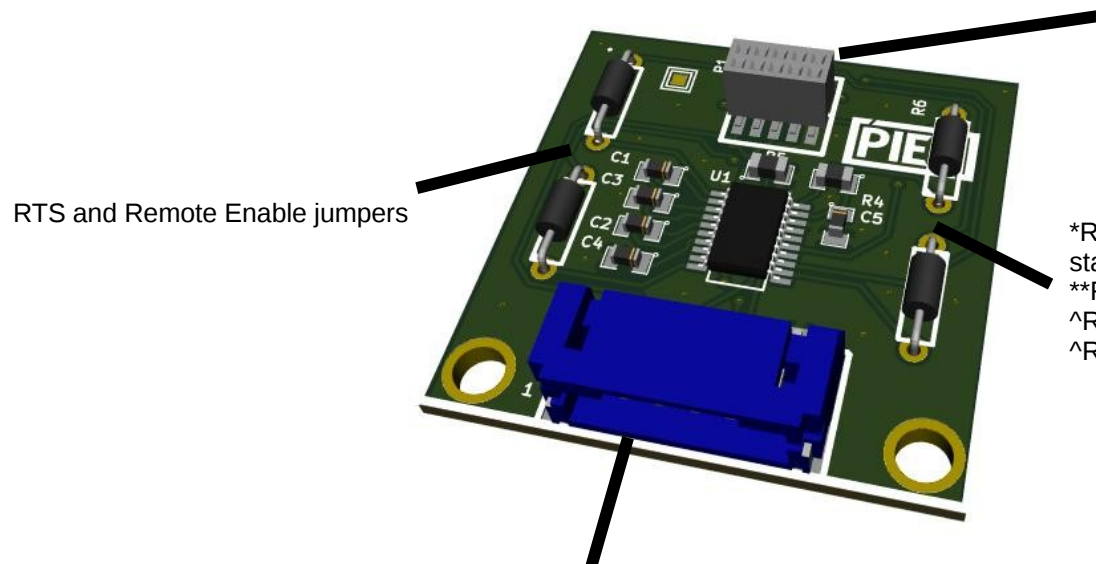




PN 311-017, Rev B

RS232, SCI Interface



Stacking Headers

Top & Bottom Headers

1	Vcc	DIO*	2
3	DIO**		4
5	DIO^	DIO^	6
7	SCI-RX	SCI-TX	8
9	GND		10

- *R3 – Remove to disable remote enable; frees SPIP-2 for other stacked peripherals
- **R5 – Remove to disable remote shutdown & free SPIP-3
- ^R1 – Disables RTS if removed & free SPIP-5
- ^R2 – Disables CTS if removed & free SPIP-6

Output Connector P2

1	Vcc	RTS	4
2	SCI-RX	CTS	5
3	SCI-TX	GND	6

- Mating P2 connector –SKU 400-005
- Pre-crimped leads – SKU 400-009
- Additional Standoffs – SKU 400-012

SP3222E based serial transceiver; 3.3V and 5V logic compliant. CTS and RTS flow control optionally implemented. Most PIEP processors implement CTS / RTS control via general purpose I/O. Other features optionally implemented include remote enable and remote shutdown. Optional features when not implemented free associated resource pins for use by other stacked peripherals.

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