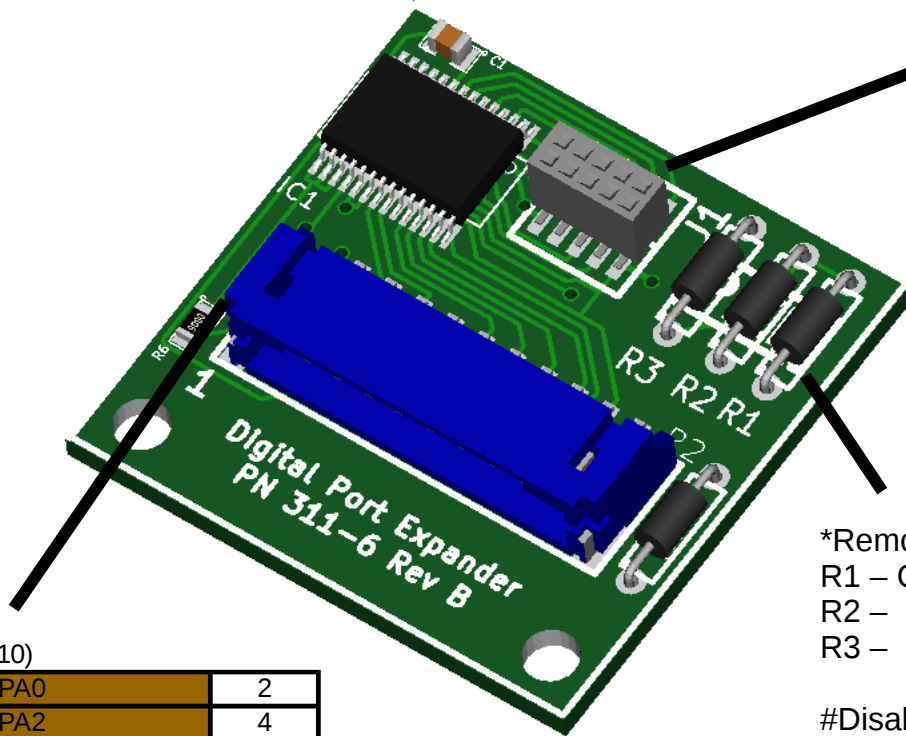




PN 311-06, Rev A Port Expander Board



Stacking Header Pin Assignments

Top

1	Vcc	GPB0	2
3	GPB1	GPB2	4
5	GPB3	GPB4	6
7	GPB5	GPB6	8
9	GND	GPB7	10

Port B Input / Output

Bottom Header

1	Vcc	DIO *	2
3	DIO *	DIO *	4
5	SPI-SCK	DIO#	6
7	SPI-MISO	SPI-MOSI	8
9	GND	IRQ	10

*Remove two of three resistors to set Chip Select
 R1 – Chip select routed through SPIP pin 2
 R2 – Chip select routed through SPIP pin 3
 R3 – Chip select routed through SPIP pin 6

#Disable Remote chip reset by cutting R7 (Frees SPIP-10 resource for other stacked peripherals).

Supplemental Header (P10)

1	Vcc	GPA0	2
3	GPA1	GPA2	4
5	GPA3	GPA4	6
7	GPA5	GPA6	8
9	GND	GPA7	10

Port A Input / Output

P10 mating connector- JST PHR-10
 Pre-crimped leads – JST ASPHSPH24K305

Microchip MC23S17 SPI interface port expander

PIEP is a copyright of E3 Embedded Systems, LLC and is protected under US Patent 9,870,337



EMBEDDED SYSTEMS