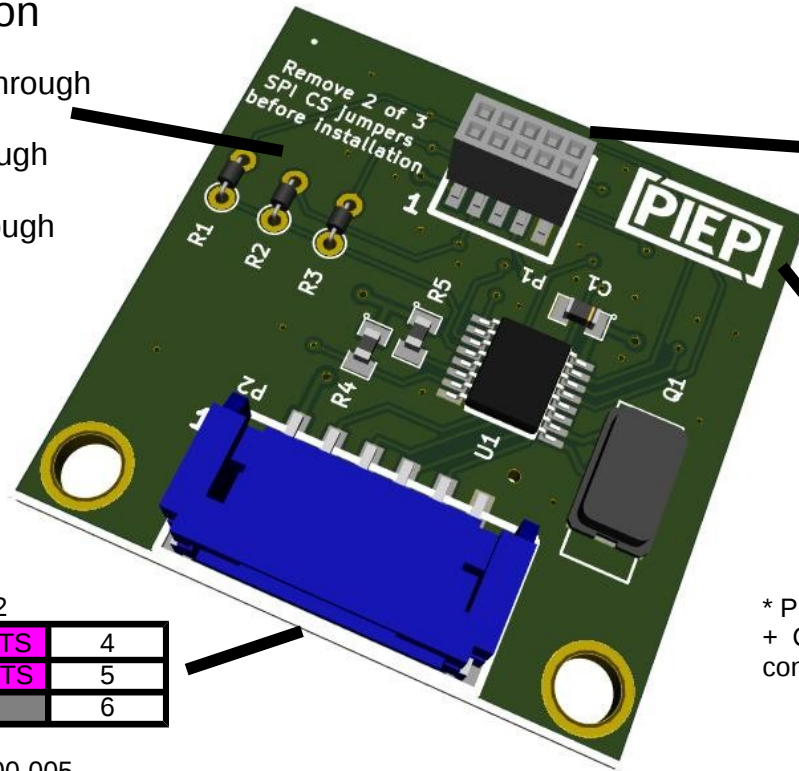




PN 311-005, Rev A SPI to SCI Board

SPI Chip Select Configuration

- + Remove R1 & R2 for SPI control through SPIP-4
- Remove R1&R3 for SPI control through SPIP-3
- Remove R2&R3 for SPI control through SPIP-2



Top Header

1	5V / 3.3V	*	2
3	*	*	4
5	SCI-CTS	SCI-RTS	6
7	SCI-RX	SCI-TX	8
9	GND	IRQ*	10

Bottom Header

1	5V / 3.3V	DIO+	2
3	DIO+	DIO+	4
5	SPI-SCK		6
7	SPI-MISO	SPI-MOS	8
9	GND	IRQ	10

Output Connector P2

1	5V / 3.3V	SCI-RTS	4
2	SCI-TX	SCI-CTS	5
3	SCI-RX	GND	6

* Pins carry through from bottom header
 + Chip Select for SPI control Refer to R1-R3 configurations

Mating P2 connector –SKU 400-005
 Pre-crimped leads – SKU 400-009
 Spare Standoffs – SKU 400-012

Max3100 based SPI to SCI (TTL level) controller. Interrupt output available for receive data buffer full and transmit data buffer empty. Compatible with PIEP SCI or USB peripheral boards

PIEP is a copyright of E3 Embedded Systems, LLC and is protected under US Patent 9,870,337

